


NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR- 190006

Walk-in interview of interested candidates for engagement as a Project Assistant for a research project "Model Order Reduction for Simulation Acceleration in Power Electronics" sponsored by the Central Power Research Institute (CPRI) Bangalore. The interview is scheduled to be held on 05-08-2019 in the Committee Room of the Department of Electrical Engineering.

Project Details:

Name & No. of Positions	Project Assistant; 01 Post.
Place	Department of Electrical Engineering, National Institute of Technology Srinagar Hazratbal 190006 J&K
Qualification	a) M. E. / M. Tech in relevant fields of Electrical Engineering (minimum CGPA: 8.0 / 80 %), or b) B. E. / B. Tech in Electrical Engineering (minimum CGPA 8.0 / 80%) and having qualified GATE. Note: Candidates who have obtained M. Tech. (Sponsored) through NIT Srinagar are not eligible unless they have two year experience post completion of their degree. Expertise in MATLAB/Simulink is essential. A thorough understanding of Control Systems, Power Electronics and Linear Algebra is desirable.
Duration	06 – 09 months
Salary	Rs. 25,000/- pm + HRA (likely to be revised to Rs. 31,000/- pm + HRA) as per institute norms. Campus Accommodation is not available.
Commitment	The candidate is expected to commit his/her availability until the completion of the project.
Selection Process	Candidates have to appear in an interview. All relevant documents should be brought along at the time of the interview No TA/DA will be paid for participating in the selection process. The selected candidate is expected to join within a week after intimation of their selection else it would be offered to a candidate in waiting list.
Contact Details	Dr. Mohammad Abid Bazaz Dept. of Electrical Engineering, NIT Srinagar Email: abid@nitsri.net Please include the following details in your CV: i) Marks obtained in Control Systems, Power Electronics and MATLAB courses. ii) GATE Score iii) Rank in class and total class strength iv) Publications/Prizes/Awards if any.


Registrar
30/7/19

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