

**NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR  
HAZRATBAL SRINAGAR KASHMIR-190006 J&K**

Advertisement Notice No. 01 of 2017

Dated :- 03-01-2017

Applications are invited for the posts of Project faculty and Lab Engineer in the project “Special Manpower Development Program Chip to System Design” (SMDP C2S) sponsored by MoCIT (Govt. of India), New Delhi. Posts are temporary, but likely to continue for the duration (4 Years) of the project. The posts are coterminous with the duration of the project “SMDP – C2S”. The project staff can undergo the PhD programme subsequently, subject to fulfillment of Institute statutes.

Interested candidates may apply with a copy of resume mentioning all details affixed with a recent passport size photograph, and degree certificates; to the Head ECE Deptt, NIT Srinagar latest by 09-02-2017.

Any applicant wishing to apply for both positions needs to submit two applications separately.

Post	Job Requirement	Qualification		Remuneration Consolidated
		Essential	Desirable	
Project Faculty	Execution of VLSI projects, Teaching VLSI related courses and related activities.	<ol style="list-style-type: none"> <li>1. B.E./ B.Tech. in ECE; with a minimum CGPA of 6.5 or not less than 60%.</li> <li>2. M.E./ M.Tech. in related field with specialization in VLSI Design/ Microelectronics or who have done Major project related to VLSI; with a minimum CGPA of 6.5 or not less than 60%.</li> <li>3. Knowledge of VLSI Software.</li> </ol>	Working Knowledge of VLSI tools like Cadence, Mentor, Tanner, Xilinx, Synopsys, Layout Generation, etc.	Rs. 40,000/- per month

Lab Engineer	Hardware and VLSI software installation and maintenance, VLSI related laboratory class conduction and related activities.	<ol style="list-style-type: none"> <li>1. B.E./ B.Tech. in ECE; with a minimum CGPA of 6.5 or not less than 60%.</li> <li>2. M.E./ M.Tech in related field with specialization in VLSI Design/ Microelectronics or who have done Major project related to VLSI; with a minimum CGPA of 6.5 or not less than 60%.</li> </ol> <p><b>OR</b></p> <ol style="list-style-type: none"> <li>1. B.E./ B.Tech. in ECE; with a minimum CGPA of 8 or not less than 75%.</li> <li>2. Valid Gate Score above the prescribed cut off level.</li> <li>3. Knowledge of VLSI Software.</li> </ol>	Working Knowledge of VLSI tools like Cadence, Mentor, Tanner, Xilinx, Synopsys, Layout Generation, etc.	Rs. 34,000/- per month
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The list of eligible applicants for written test and / or Interview will be uploaded on the website: [www.nitsri.net](http://www.nitsri.net). after the short listing/ screening of the applications.

Sd/= Registrar

No.NIT/PD/17/248-61  
Dated:- 19-01-2017